

IN THE CLAIMS:

This listing of claims will replace all prior versions and listing of claims in the application:

LISTING OF CLAIMS:

1. (Canceled)

2. (Currently Amended) A fault tolerant graphics controller [[as recited in claim

1,]]comprising:

an error code generator that receives graphics command codes, the error code generator configured to generate an error code for each graphics command code received;

a frame buffer for storing graphics data and graphics commands, including the error codes generated by the error code generator;

an error code decoder connected between the frame buffer and the graphics controller
and

configured to determine from a command code and its associated error code whether or not there is an error in the command code and to provide an indication thereof; and

a graphics controller for executing the graphics command codes to display the graphics data;

wherein the error code generator is a software module that is configured to store the received graphics commands into an array and to compute for each command a portion of an error code, such that, after a given number of commands is received, a check code for the given number of commands and the commands are available for processing by a CPU.

3. (Original) A fault tolerant graphics controller as recited in claim 2, wherein the given number of commands is eight and the check code has a size in bits and the commands have a size in bits that are equal.

4. (Currently Amended) A fault tolerant graphics controller as recited in claim [[1]]2,

wherein the error code generator receives data associated with graphics commands and

is further configured to generate an error code for graphics data received; and

wherein the error code decoder is configured to determine from graphics data and its associated error code whether or not there is an error in the graphics data and to provide an indication thereof.

5. (Currently Amended) A fault tolerant graphics controller[[as recited in claim 1,]]
comprising:

an error code generator that receives graphics command codes, the error code generator configured to generate an error code for each graphics command code received;

a frame buffer for storing graphics data and graphics commands, including the error codes generated by the error code generator;

an error code decoder connected between the frame buffer and the graphics controller and configured to determine from a command code and its associated error code whether or not there is an error in the command code and to provide an indication thereof; and

a graphics controller for executing the graphics command codes to display the graphics data;

wherein the error code generator is configured to generate error codes, each 32 bits, for a group of eight graphics command codes, each 256 bits, and to combine the eight error codes together into a single 256 bit unit, for storage in the frame buffer along with the eight command codes.

6. (Original) A fault tolerant graphics controller as recited in claim 5, wherein the eight command codes and the combined eight error codes form a group of nine units of information, each 256 bits, to be stored as a unit in the frame buffer.

7. (Original) A fault tolerant graphics controller as recited in claim 6, wherein read and write pointers are included within the combined error code to provide demarcation of the individual error codes therein.

8. (Original) A fault tolerant graphics controller as recited in claim [1]2, wherein the error code decoder is configured to determine that a detected error in the command code is correctable

and the decoder is further configured to correct the error in the command code.

9. (Original) A fault tolerant graphics controller as recited in claim [1]2, further comprising an interrupt and status controller, connected to the error code decoder, the interrupt and status controller configured to generate an interrupt or error status to a computer system hosting the graphics controller in response to the error indication from the error code decoder.

10. (Original) A fault tolerant graphics controller as recited in claim [1]2,

wherein the error indication from the error code decoder indicates that the error is uncorrectable; and

further comprising an interrupt and status controller connected to the error code decoder, the interrupt and status controller configured to generate an interrupt or error status to a computer hosting the graphics controller in response to the error indication when said indication indicates the error is uncorrectable.

11. (Original) A fault tolerant graphics controller as recited in claim [1]2, further comprising a flush-command controller connected to the error code decoder, the flush-command controller configured to flush command codes with errors from the graphics controller in response to the error indication.

12. (Original) A fault tolerant graphics controller as recited in claim 11, wherein the flush-command controller is configured to flush data associated with any erroneous command that is flushed.

13. (Currently Amended) A fault tolerant graphics controller as recited in claim [[1]]2,

wherein the error indication from the error code decoder indicates that the error is uncorrectable; and

further comprising a flush-command controller connected to the error code decoder, the flush-command controller configured to flush command codes with errors from the graphics controller in response to the error indication when said indication indicates the error is uncorrectable.

14. (Original) A fault tolerant graphics controller as recited in claim 13, wherein the flush-command controller is configured to flush data associated with any erroneous command that is flushed.

15. (Currently Amended) A fault tolerant graphics controller as recited in claim [[1]]2, further comprising a store-command controller connected to the error code decoder, the store-command controller configured to store command codes before and after the command code determined to have an error in response to the error indication.

16. (Currently Amended) A fault tolerant graphics controller as recited in claim [[1]]2, wherein the error indication from the error code decoder indicates that the error is uncorrectable; and
further comprising a store-command controller connected to the error code decoder, the store-command controller configured to store command codes before and after the command code determined to have an error in response to the error indication when said indication indicates that the error is uncorrectable.

17. (Canceled)]

18. (Canceled)]

19. (Currently Amended) A method for processing graphics commands, as recited in claim [[17]]25, further comprising generating an interrupt or error status in response to an error indication.

20. (Currently Amended) A method for processing graphics commands, as recited in claim [[17]]25, further comprising generating an interrupt or error status in response to an error indication when said indication indicates that the error is uncorrectable.

21. (Currently Amended) A method for processing graphics commands, as recited in claim

[[17]]25, further comprising preventing the execution of command codes in response to an error indication.

22. (Currently Amended) A method for processing graphics commands, as recited in claim [[17]]25, further comprising preventing the execution of command codes in response to an error indication when said indication indicates that the error is uncorrectable.

23. (Currently Amended) A method for processing graphics commands, as recited in claim [[17]]25, further comprising storing commands before and after a command having an erroneous code in response to an error indication.

24. (Currently Amended) A method for processing graphics commands, as recited in claim [[17]]25, further comprising storing commands before and after a command having an erroneous code in response to an error indication when said indication indicates that the error is uncorrectable.

25. (Currently Amended) A method for processing graphics commands[[, as recited in claim 17,]]comprising:

receiving a graphics command code;

generating an error code for the graphics command code;

storing the graphics command code and its associated error code in a frame buffer configured to store graphics command codes and graphics data;

accessing the frame buffer for the graphics command code and obtaining its associated error code;

determining from the graphics command code and its associated error code whether there is an error in the graphics command code and providing an indication thereof;

executing the graphics commands to display the graphics data;

wherein a group of graphics command codes is received; and

wherein generating an error code for the graphics command code includes generating an error code for each command code in the group[[:]], and [[further comprising]] combining the error codes together into a single code word having the same size as any of the graphics

command codes.

26. (Original) A method for processing graphics commands, as recited in claim 25, wherein the group includes eight graphics command codes, each 256 bits; and wherein each error code is 32 bits and the combined error code is 256 bits.